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<u>L8</u> (ic or (integrated adj circuit)) adj2 (chip or card)	163325	<u>L8</u>
<u>L7</u> (instruction or command) adj2 code	40667	<u>L7</u>

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<u>L5</u>	id or identification or name or number	8278117	<u>L5</u>
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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Neobit/sup /spl reg// - high reliable logic non-volatile memory (NVM)**
 Wang, R.S.C.; Shen, R.S.J.; Hsu, C.C.H.;
Physical and Failure Analysis of Integrated Circuits, 2004. IPFA 2004. Proceedings of the 11th International Symposium on the
 5-8 July 2004 Page(s):111 - 114
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- ☐ 2. **Neobit/sup /spl reg// - high reliable logic non-volatile memory (NVM)**
 Wang, R.S.C.; Shen, R.S.J.; Hsu, C.C.H.;
Physical and Failure Analysis of Integrated Circuits, 2004. IPFA 2004. Proceedings of the 11th International Symposium on the
 5-8 July 2004 Page(s):111 - 114
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- ☐ 3. **A Passive UHF RF Identification CMOS Tag IC Using Ferroelectric RAM in 0.35- μ m Technology**
 Nakamoto, H.; Yamazaki, D.; Yamamoto, T.; Kurata, H.; Yamada, S.; Mukaida, K.; Ninomiya, T.; Ohkawa, T.; Masui, S.; Gotoh, K.;
Solid-State Circuits, IEEE Journal of
 Volume 42, Issue 1, Jan. 2007 Page(s):101 - 110
 Digital Object Identifier 10.1109/JSSC.2006.886523
[AbstractPlus](#) | Full Text: [PDF\(1662 KB\)](#) IEEE JNL
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- ☐ 4. **High-Temperature, High Reliability EEPROM Design For Automotive Applications**
 Walsh, J.; Scott, G.;
Conference 2006, IEEE Custom Integrated Circuits
 10-13 Sept. 2006 Page(s):449 - 452
 Digital Object Identifier 10.1109/CICC.2006.320818
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- ☐ 5. **Design and applications of ferroelectric nonvolatile SRAM and flip-flop with unlimited read/program cycles and stable recall**
 Masui, S.; Yokozeki, W.; Oura, M.; Ninomiya, T.; Mukaida, K.; Takayama, Y.; Teramoto, T.;
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- ☐ **6. Macromodel development for a FLOTOX EEPROM**
Noren, K.V.; Ming Meng;
[Electron Devices, IEEE Transactions on](#)
Volume 45, Issue 1, Jan. 1998 Page(s):224 - 229
Digital Object Identifier 10.1109/16.658835
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- ☐ **7. Contrasting single-wafer and batch processing for memory devices**
Weimer, R.A.; Eppich, D.M.; Beaman, K.L.; Powell, D.C.; Gonzalez, F.;
[Semiconductor Manufacturing, IEEE Transactions on](#)
Volume 16, Issue 2, May 2003 Page(s):138 - 146
Digital Object Identifier 10.1109/TSM.2003.810939
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- ☐ **8. CMOS passive RFID transponder with read-only memory for low cost fabrication**
Jeon, W.; Melngailis, J.; Newcomb, R.W.;
[SOC Conference, 2005. Proceedings. IEEE International](#)
25-28 Sept. 2005 Page(s):181 - 184
Digital Object Identifier 10.1109/SOCC.2005.1554490
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